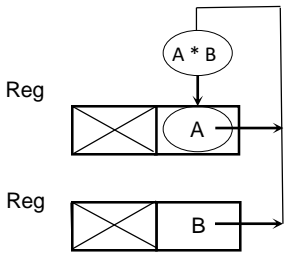
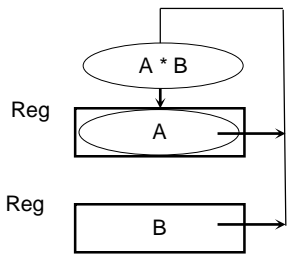


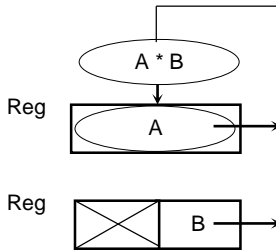
MS (RX)  
MSY (RXY)



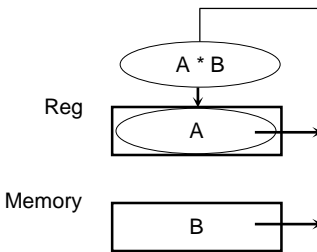
MSR (RRE)



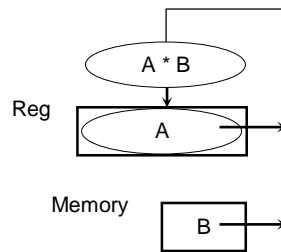
MSGR (RRE)



MSGFR (RRE)

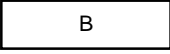


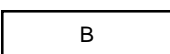





MSG (RXY)



MSGF (RXY)

### Legend:

- Reg  64-Bit Register
- Reg  Register Bits 32-63
- Memory  Fullword Memory
- Memory  Quadword Memory
-  Unused Register Bits 0-31
-  64-Bit Result
-  32-Bit Result